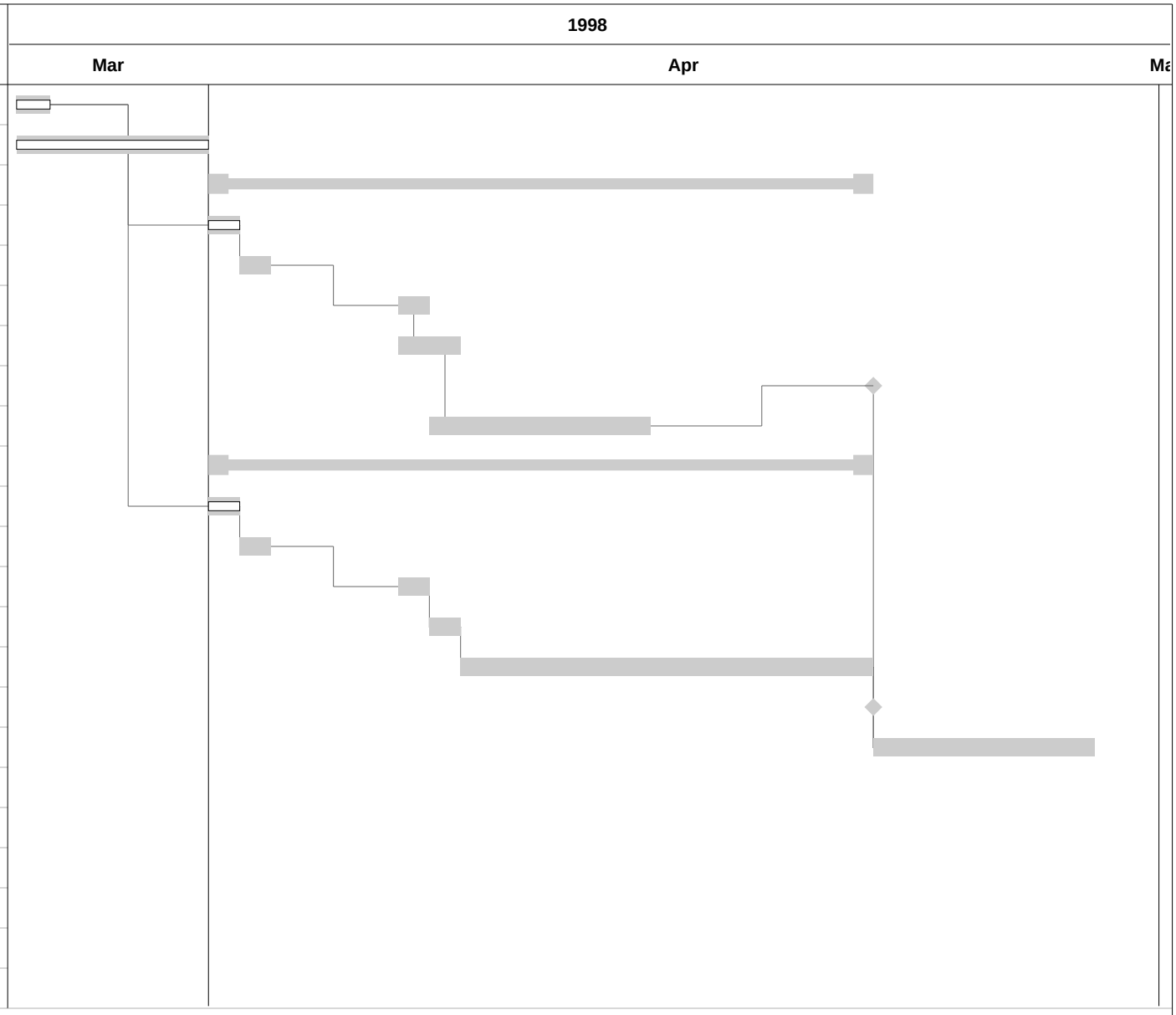
Task #	Name	Duration
1	Do schedule	1.00
2	Understand specifications	2.00
3	HW simulator	9.00
4	design in-core objects	1.00
17	input/output exec format	1.00
5	write main loop	0.50
6	input packet bits	1.00
7	hw simulator finished	1.00
8	module testing	2.00
9	Linker	9.00
10	understand tools from ..	1.00
11	input object libraries	1.00
12	API/script	1.00
13	instantiate filters	1.00
14	rewrite to n-way execut..	5.00
15	working linker	0.00
16	integrate linker and simul..	3.00



Project: Classifier HW simulator

Date : 1APR1998

Task		Critical Task		Completion	
Milestone		Critical Milestone		Target Dates	
Summary Task		Critical Summary Task			